

Abstract of the Disclosure

[0034] A host-NVRAM disk-array controller that can be connected to a host computer. The controller including an NVRAM, a plurality of disk array controllers, and a plurality of busses. The NVRAM is connected to a memory controller (together called the NVRAM device). The host computer has the ability to directly control the NVRAM device. The plurality of busses connect the NVRAM device and the disk array controllers.